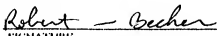


PTO/PCT Rec'd 05 NOV 2001

FORM PTO-1300 JULY 7/2001		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTORNEY'S DOCKET NUMBER AZ.2968 U.S. APPLICATION FILED: 11/05/2000, SEE 37 CFR 1.5	
TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DOE/EOUS) CONCERNING A FILING UNDER 35 U.S.C. 371					
INTERNATIONAL APPLICATION NO. PCT/EP00/03666		INTERNATIONAL FILING DATE April 22, 2000		PRIORITY DATE CLAIMED May 6, 1999	
TITLE OF INVENTION UV-SUPPORTED THERMAL TREATMENT OF COMPOUND SEMICONDUCTORS IN RTP SYSTEMS					
APPLICANT(S) FOR DOE/EOUS MARTIN DRECHSLER AND ARTHUR PELZMANN					
Applicant herewith submits to the United States Designated/Elected Office (DOE/EOUS) the following items and other information:					
1. <input checked="" type="checkbox"/> This is a FIRST submission of items concerning a filing under 35 U.S.C. 371. 2. <input type="checkbox"/> This is a SECOND or SUBSEQUENT submission of items concerning a filing under 35 U.S.C. 371. 3. <input checked="" type="checkbox"/> This is an express request to begin national examination procedures (35 U.S.C. 371(f)). The submission must include items (5), (6), (9) and (21) indicated below. 4. <input checked="" type="checkbox"/> The US has been elected by the expiration of 19 months from the priority date (Article 31). 5. <input checked="" type="checkbox"/> A copy of the International Application as filed (35 U.S.C. 371(c)(2)) a. <input type="checkbox"/> is attached hereto (required only if not communicated by the International Bureau). b. <input checked="" type="checkbox"/> has been communicated by the International Bureau. c. <input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US). 6. <input checked="" type="checkbox"/> An English language translation of the International Application as filed (35 U.S.C. 371(c)(2)). a. <input type="checkbox"/> is attached hereto. b. <input type="checkbox"/> has been previously submitted under 35 U.S.C. 154(d)(4). 7. <input type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3)) a. <input type="checkbox"/> are attached hereto (required only if not communicated by the International Bureau). b. <input type="checkbox"/> have been communicated by the International Bureau. c. <input type="checkbox"/> have not been made; however, the time limit for making such amendments has NOT expired. d. <input type="checkbox"/> have not been made and will not be made. 8. <input type="checkbox"/> An English language translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)). 9. <input type="checkbox"/> An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(1)). 10. <input type="checkbox"/> An English language translation of the annexes of the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)). Items 11 to 20 below concern document(s) or information included: 11. <input checked="" type="checkbox"/> An Information Disclosure Statement under 37 CFR 1.97 and 1.98. 12. <input type="checkbox"/> An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included. 13. <input checked="" type="checkbox"/> A FIRST preliminary amendment. 14. <input type="checkbox"/> A SECOND or SUBSEQUENT preliminary amendment. 15. <input type="checkbox"/> A substitute specification. 16. <input type="checkbox"/> A change of power of attorney and/or address letter. 17. <input type="checkbox"/> A computer-readable form of the sequence listing in accordance with PCT Rule 13ter.2 and 35 U.S.C. 1.821 - 1.825. 18. <input type="checkbox"/> A second copy of the published international application under 35 U.S.C. 154(d)(4). 19. <input type="checkbox"/> A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4). 20. <input type="checkbox"/> Other items or information:					

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Rosalie A. Centeno

U.S. APPLICATION NO. 10/069826 INTERNATIONAL APPLICATION NO. PCT/EP00/03666		ATTORNEY'S DOCKET NUMBER AZ. 2968	
21. <input checked="" type="checkbox"/> The following fees are submitted: BASIC NATIONAL FEE (37 CFR 1.492 (a) (1) - (5)): Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.413(a)(2)) paid to USPTO and International Search Report not prepared by the EPO or JPO. \$1040.00 International preliminary examination fee (37 CFR 1.482) not paid to USPTO but International Search Report prepared by the EPO or JPO \$090.00 International preliminary examination fee (37 CFR 1.482) not paid to USPTO but international search fee (37 CFR 1.415(a)(2)) paid to USPTO \$740.00 International preliminary examination fee (37 CFR 1.482) paid to USPTO but all claims did not satisfy provisions of PCT Article 33(1) (4) \$710.00 International preliminary examination fee (37 CFR 1.482) paid to USPTO and all claims satisfied provisions of PCT Article 33(1) (4) \$100.00 ENTER APPROPRIATE BASIC FEE AMOUNT =		CALCULATIONS PTO USE ONLY \$ 890.00	
Surcharge of \$130.00 for furnishing the oath or declaration later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492(e)). \$		\$	
CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE
Total claims	20 - 20 =	0	x \$18.00
Independent claims	1 - 3 =	0	x \$84.00
MULTIPLE DEPENDENT CLAIM(S) (if applicable)			x \$200.00
TOTAL OF ABOVE CALCULATIONS =			\$ 890.00
<input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27. The fees indicated above are reduced by 1/2.			\$
SUBTOTAL:			\$ 890.00
Processing fee of \$130.00 for furnishing the English translation later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492(f)).			\$
TOTAL NATIONAL FEE =			\$ 890.00
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.41). \$40.00 per property			\$
TOTAL FEES ENCLOSED =			\$ 890.00
Amount to be refunded:			\$
charged:			\$
a. <input checked="" type="checkbox"/> A check in the amount of \$ 890.00 to cover the above fees is enclosed. b. <input type="checkbox"/> Please charge my Deposit Account No. _____ in the amount of \$ _____ to cover the above fees. A duplicate copy of this sheet is enclosed. The Commissioner is hereby authorized to charge any additional fees which may be required, or credit to any overpayment to Deposit Account No. 02-1653. A duplicate copy of this sheet is enclosed. In the event there is any discrepancy in the amount sent herewith or at any time in the future please charge my additional fee, credit or overpayment to the above deposit account number. d. <input type="checkbox"/> Fees are to be charged to a credit card. WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.			
NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137 (a) or (b)) must be filed and granted to restore the application to pending status.			
SEND ALL CORRESPONDENCE TO: <div style="display: flex; justify-content: space-between;"> <div> ROBERT W. BECKER & ASSOCIATES 11896 N. Highway 14, Suite B Tijeras, NM 87059 </div> <div style="text-align: right;">  SIGNATURE Robert W. Becker NAME 26,255 REGISTRATION NUMBER </div> </div>			

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Rosalie A. Centeno
Rosalie A. Centeno, Secretary

In the Application of Martin Drechsler et al

Ser.No.: Not Yet Known (Based on PCT/EP00/03666 filed April 22, 2000 and German priority documents 199 20 871.9 filed 06 May 1999)

For: UV-Supported Activation of Doping Agent in Compound Semiconductors Via RTP Systems

Filed on: November 5, 2001

Assistant Commissioner for Patents

Washington, DC 20231

PRELIMINARY AMENDMENT ACCOMPANYING PCT NATIONAL STAGE APPLICATION

Sir:

Prior to examination, please amend the above-identified application as follows.

IN THE SPECIFICATION:

On page 1, please replace the title to read as follows:

--UV-Supported Thermal Treatment of Compound Semiconductors in RTP Systems--

On page 1, immediately after the title, please insert the following heading:

--Background of the Invention--.

On page 2, at line 10, please delete "650 C to 800 C", and replace with "650° C to 800° C", so that paragraph reads as follows:

--A method according to the initially described type for activating the hydrogen acceptor complexes is described in US 5,786,233, whereby during the method the substrate is irradiated with short wave light, the photon energy of which is greater than the energy gap or band gap at the

process temperature. The substrates are preferably heated to temperatures of approximately 650° C to 800° C for a duration of two to thirty minutes. In so doing, the complexes are broken up and the previously passivated acceptors (e.g. GaN:Mg) are activated, as a result of which the sheet resistance is reduced by up to several orders of magnitude, and the hole carrier concentration is correspondingly increased, respectively. As a result of the irradiation with short wavelength light, the activation of the acceptors and hence the hole carrier concentration were significantly increased. Since at still higher temperatures, however, the layer is thermally damaged and the p-diffusivity again decreases as the treatment duration increases, pursuant to US 5,786,233 a longer annealing is preferably used at comparatively low temperatures--.

On page 3, at line 4, please delete "550", and replace with "550°", and at line 6, please delete "100 C" and replace with "100° C", so that entire paragraph reads as follows:

--Yoichi Kamiura et al.; Jpn.J.Appl.Phys. 37 (1998) L970-L971 describes the influence of UV irradiation upon the Mg activation of GaN films. In this connection, the GaN films are held in a furnace for about one hour at a temperature greater than an activation temperature of about 550° C. Upon irradiation of the Mg doped GaN layer with UV radiation, the activation temperature can be reduced approximately by 100° C, as a result of which the thermal stress of the substrates is similarly reduced--.

On page 3, at line 11, please delete "600 C", and replace with "600° C", so that entire paragraph reads as follows:

--In EP 0723303, there is described a light-emitting electronic component that is built up of a hetero structure, and a method for producing the same, according to which at approximately 600° C with the aid of UV laser radiation, an annealing is carried out in order to increase the acceptor activation in the layers and to reduce the sheet resistance, respectively--.

On page 4, line 8, please insert the following heading:

--Summary of the Invention--.

On page 5, at line 6, please delete "350", and replace with "350°", at line 7, please delete "900 C" and replace with "900° C", and at line 8, please delete "350 C and 600 C" and replace with "350° C and 600° C", so that entire paragraph reads as follows:

--The first temperature of the inventive method is selected between 350° C and 900° C; whereby for example with Mg-containing GaN (or in general with group III nitrides) a temperature between 350° C and 600° C is preferred. Depending upon the type of semiconductor, the first temperature can also be a function of the selection of the third time interval and of the intensity of the electromagnetic radiation and of the generation of minority charge carriers connected therewith. As the length of the third time interval increases, and with increasing intensity of the electromagnetic radiation, then depending upon the type of semiconductor the first temperature can be decreased, which advantageously leads to a reduction of the thermal stress of the layer--.

On page 5, at line 19, please delete "700 C and 1400 C", and replace with "700° C and 1400° C", and at line 22, please delete "850 C and 1200 C" and replace with "850° C and 1200° C", so that entire paragraph reads as follows:

--The second temperature during the second time interval is preferably selected between 700° C and 1400° C. The selection of this temperature depends to a considerable extent upon the material of the compound semiconductor, whereby for example with Mg-containing GaN a second temperature between 850° C and 1200° C is preferably selected. By selecting a higher second temperature, the second time interval can preferably be considerably shortened, which similarly again leads to a reduction of the thermal stressing of the semiconductor layer or of the semiconductor layer system.--

On page 8, at line 23, please delete "1400 C", and replace with "1400° C", so that entire paragraph reads as follows:

The inventive method is advantageously carried out, as mentioned above, by means of a rapid heating system (RTP System), since by means of the RTP system defined very short heating

processes can be carried out in the range of one second up to 30 minutes. The temperature-time curves of the substrates can be established precisely to the second in a temperature range of room temperature to about 1400° C, whereby the substrate can be heated extremely uniformly not only at low temperatures but also at high temperatures. Similarly, in RTP systems various process gases, which surround the substrate, can also be used, whereby the process gas pressure can be adjusted from vacuum conditions to overpressure conditions.

On page 9, at line 5, please insert the following heading:

--Brief Description of the Drawings--;

On page 10, at line 3, please insert the following heading:

--Description of Preferred Embodiments--.

On page 10, line 3, please delete "curve a" and replace with "curve a," at line 6 please delete "550 C)" and replace with "550° C)", at line 18, please delete "550 C" and replace with "550° C", and at line 19, please delete "1050 C." and replace with "1050° C.", so that entire paragraph reads as follows:

In Fig. 1, curve a, schematically shows a typical plot of a sheet resistance of a compound semiconductor, e.g. a GaN film that is treated or doped with Mg, as a function of the temperature. At low temperatures (e.g. GaN at less than approximately 550° C), the semiconductor film has a high electrical resistance R due to the passivation of the foreign atoms (e.g. Mg) by hydrogen. As the temperature increases, the resistance is reduced since more and more hydrogen-acceptor (foreign atom) complexes break up and the acceptors are thus electrically activated. If all of the complexes are broken up, and all acceptors activated, as the temperature increases from a lower temperature value T_L to an upper temperature value T_H , the sheet resistance remains nearly constant. Above the upper temperature value T_H , the sheet resistance R again significantly increases as the temperature increases, since above this temperature T_H a decomposition of the semiconductor layer occurs. For Mg doped GaN, at atmospheric pressure, T_L is approximately

550° C and T_H is approximately 1050° C.

On page 11, line 8, please delete "100 C" and replace with "100° C", and at line 11, please delete "curve b." and replace with "curve b." so that entire paragraph reads as follows:

By generating minority charge carriers, the temperature T_L can be reduced. In this connection, the charge carriers can be generated, for example, by irradiation with UV light, whereby the irradiated photon energy is greater than the energy gap of the semiconductor layer at the appropriate layer temperature. However, the charge carriers can also be generated, for example, by applying a potential or voltage to a semiconductor layer or to a semiconductor layer system, or by a combination of UV irradiation and applying a potential or a voltage to the layer or the layer system. The reduction of the lower temperature value T_L depends upon the number of charge carriers generated. The lower temperature value can be lowered by approximately 100° C by means of UV irradiation, for example in the case of GaN with a wave length of less than approximately 360nm as illustrated in Fig 1 by the curve b--.

On page 12, at line 3, please delete "200 C" and replace with "200° C", at line 4, please delete "550 C" and replace with "550° C", at line 5, please delete "1050 C," and replace with "1050° C.", and on page 13, line 6, please delete "75 C/s and 500 C/s." and replace with "75° C/s and 500° C/s.", so that entire paragraph reads as follows:

-- Fig. 2a shows an example of a temperature-time curve of the inventive method. The semiconductor layer or in general the semiconductor layer system is heated from room temperature (point A) as rapidly as possible to the temperature T_1 , at which the sheet resistance of the semiconductor, or the sheet resistance of at least one layer of the semiconductor system, decreases (point B). Now for a first time interval of less than 120 seconds (time difference BE between B and E), the temperature of the layer (or at least one layer of the layer system) is held above the temperature T_1 . Within this first time interval, the layer is heated for the duration of a second time interval (time difference CD between point D and C) to a second temperature T_2 ,

whereby the second time interval is less than 60 seconds. Within this second time interval, the temperature-time curve can in general have any curve with fluctuations of up to 200°C about the second temperature T_2 . Figure 2a shows for GaN a T_1 of about 550°C and a T_2 greater than the decomposition temperature of about 1050°C , for which reason the second time interval CD is less than 60 seconds, preferably less than 30 seconds. The lower threshold value for the second time interval is in general determined by the (closed loop) control rate with which the increasing portion of the temperature-time curve (ramp up) can be changed into the decreasing portion (ramp down). For modern RTP systems, this time is approximately 1 second. Under certain conditions, this value can be lowered with future improvements of the RTP systems. The duration of the second time interval CD (in Fig 2a) decidedly determines how much the temperature T_2 can exceed the decomposition temperature T_H without permanently damaging the semiconductor layer or the layer system. The shorter the second time interval CD, the greater can be the temperature T_2 , since then the overall thermal stressing of the layer does not exceed a critical value in the time interval CD. If the thermal stress is below this critical value, then depending upon the type of semiconductor, the vacancies and defects that are formed can to the greatest extent or at least in part be again eliminated by a subsequent annealing (tempering) at a temperature less than T_H . The critical value of the thermal stress of the layer or of the layer system is to be determined experimentally. So that as high a temperature T_2 as possible can be achieved at prescribed critical thermal stress, it is important that the RTP system enable a rapid heating up or cooling off of the semiconductor. Typical maximum heating rates are, depending upon the unit, between 75°C/s and 500°C/s . During the process illustrated in Fig 2a, charge carriers are generated within the layer for the duration of at least one third time interval by electromagnetic radiation, preferably by UV radiation. In this connection, the third time interval can be equal to the first time interval BE, as a result of which the free charge carriers are produced only if the Mg-(or in general foreign atom) hydrogen complexes are already nearly thermally broken up. Due to the (photo) generated

free electrons, the diffusion characteristic of the broken-up hydrogen within the semiconductor layer can then be influenced. Alternatively, the third time interval can also embrace the second BE or the overall process AF. In general, the third time interval can be anywhere within the process AF, whereby its duration can be up to the overall process time AF. The lower limit of the third time interval is limited by the technical possibilities of providing an adequate UV light capacity. At the present this limit is approximately 10^{-9} s for pulsed lasers and approximately 10^{-6} s for flash lamps. However, time intervals of 1 to 120 seconds are preferred. It is particularly advantageous to set the third time interval during the cooling-off phase such that during the cooling of the layer, a repassivation of the foreign atoms (e.g. the Mg in GaN) is extensively prevented. Furthermore, it is also advantageous during the heating-up phase, for example in the range AB or AC, to produce charge carriers by UV irradiation, as a result of which, as illustrated in Fig. 1, the first temperature T_1 , and hence also the thermal stress of the semiconductor, can be reduced. Furthermore, within the process AF, charge carriers can be produced by UV radiation in a plurality of (third) time intervals. For example, the aforementioned advantages can be combined by producing charge carriers between AB or AC in ramp up and in ramp down.--

On page 18, after line 9, please insert the following paragraphs:

--The specification incorporates by reference the disclosure of German priority document 199 20 871.9 filed 06 May 1999 and International Application PCT/EP00/03666 filed 22 April 2000.

The present invention is, of course, in no way restricted to the specific disclosure of the specification and drawings, but also encompasses any modifications within the scope of the appended claims.--

IN THE CLAIMS:

Please cancel claims 1 - 19, and replace them with the attached claims 20 - 39.

REMARKS

Claims 20 - 39 are pending in the application.

Appropriate headings have been added to the specification, and claims from the literal translation have been replaced by claims drafted in conformity with U.S. Patent practice.

The application in its amended state is believed to be in condition for allowance. However, should the Examiner have any comments or suggestions, or wish to discuss the merits of the application, the undersigned would very much welcome a telephone call in order to expedite placement of the application into condition for allowance.

Respectfully submitted,



Robert W. Becker, Reg. No. 26,255
for Applicants
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Facsimile: (505) 286-3524

RWB:rac

VERSION WITH MARKINGS TO SHOW CHANGES MADE**IN THE SPECIFICATION:**

On page 2, line 10, please amend the paragraph to read as follows:

A method according to the initially described type for activating the hydrogen acceptor complexes is described in US 5,786,233, whereby during the method the substrate is irradiated with short wave light, the photon energy of which is greater than the energy gap or band gap at the process temperature. The substrates are preferably heated to temperatures of approximately 650° C to 800° C for a duration of two to thirty minutes. In so doing, the complexes are broken up and the previously passivated acceptors (e.g. GaN:Mg) are activated, as a result of which the sheet resistance is reduced by up to several orders of magnitude, and the hole carrier concentration is correspondingly increased, respectively. As a result of the irradiation with short wavelength light, the activation of the acceptors and hence the hole carrier concentration were significantly increased. Since at still higher temperatures, however, the layer is thermally damaged and the p-diffusivity again decreases as the treatment duration increases, pursuant to US 5,786,233 a longer annealing is preferably used at comparatively low temperatures--.

On page 3, at line 4, please amend paragraph to read as follows:

--Yoichi Kamiura et al.; Jpn.J.Appl.Phys. 37 (1998) L970-L971 describes the influence of UV irradiation upon the Mg activation of GaN films. In this connection, the GaN films are held in a furnace for about one hour at a temperature greater than an activation temperature of about 550° C. Upon irradiation of the Mg doped GaN layer with UV radiation, the activation temperature can be reduced approximately by 100° C, as a result of which the thermal stress of the substrates is similarly reduced--.

On page 3, at line 11, please amend paragraph to read as follows:

--In EP 0723303, there is described a light-emitting electronic component that is built up of a hetero structure, and a method for producing the same, according to which at approximately 600° C with the aid of UV laser radiation, an annealing is carried out in order to increase the acceptor activation in the layers and to reduce the sheet resistance, respectively--.

On page 5, at line 6, please amend paragraph to read as follows:

--The first temperature of the inventive method is selected between 350° C and 900° C, whereby for example with Mg-containing GaN (or in general with group III nitrides) a temperature between 350° C and 600° C is preferred. Depending upon the type of semiconductor, the first temperature can also be a function of the selection of the third time interval and of the intensity of the electromagnetic radiation and of the generation of minority charge carriers connected therewith. As the length of the third time interval increases, and with increasing intensity of the electromagnetic radiation, then depending upon the type of semiconductor the first temperature can be decreased, which advantageously leads to a reduction of the thermal stress of the layer--.

On page 5, at line 19, please amend paragraph to read as follows:

--The second temperature during the second time interval is preferably selected between 700° C and 1400° C. The selection of this temperature depends to a considerable extent upon the material of the compound semiconductor, whereby for example with Mg-containing GaN a second temperature between 850° C and 1200° C is preferably selected. By selecting a higher second temperature, the second time interval can preferably be considerably shortened, which similarly again leads to a reduction of the thermal stressing of the semiconductor layer or of the semiconductor layer system.--

On page 8, at line 23, please amend paragraph to read as follows:

The inventive method is advantageously carried out, as mentioned above, by means of a rapid heating system (RTP System), since by means of the RTP system defined very short heating

processes can be carried out in the range of one second up to 30 minutes. The temperature-time curves of the substrates can be established precisely to the second in a temperature range of room temperature to about 1400° C, whereby the substrate can be heated extremely uniformly not only at low temperatures but also at high temperatures. Similarly, in RTP systems various process gases, which surround the substrate, can also be used, whereby the process gas pressure can be adjusted from vacuum conditions to overpressure conditions.

On page 9, between lines 5 and 6, please insert the following heading:

--Brief Description of the Drawings--;

On page 10, between lines 2 and 3, please insert the following heading:

--Description of Preferred Embodiments--.

On page 10, line 3, please amend paragraph to read as follows:

In Fig. 1, curve a, schematically shows a typical plot of a sheet resistance of a compound semiconductor, e.g. a GaN film that is treated or doped with Mg, as a function of the temperature. At low temperatures (e.g. GaN at less than approximately 550° C), the semiconductor film has a high electrical resistance R due to the passivation of the foreign atoms (e.g. Mg) by hydrogen. As the temperature increases, the resistance is reduced since more and more hydrogen-acceptor (foreign atom) complexes break up and the acceptors are thus electrically activated. If all of the complexes are broken up, and all acceptors activated, as the temperature increases from a lower temperature value T_L to an upper temperature value T_H , the sheet resistance remains nearly constant. Above the upper temperature value T_H , the sheet resistance R again significantly increases as the temperature increases, since above this temperature T_H a decomposition of the semiconductor layer occurs. For Mg doped GaN, at atmospheric pressure, T_L is approximately 550° C and T_H is approximately 1050° C.

On page 11, line 8, please amend paragraph to read as follows:

By generating minority charge carriers, the temperature T_L can be reduced. In this

connection, the charge carriers can be generated, for example, by irradiation with UV light, whereby the irradiated photon energy is greater than the energy gap of the semiconductor layer at the appropriate layer temperature. However, the charge carriers can also be generated, for example, by applying a potential or voltage to a semiconductor layer or to a semiconductor layer system, or by a combination of UV irradiation and applying a potential or a voltage to the layer or the layer system. The reduction of the lower temperature value T_L depends upon the number of charge carriers generated. The lower temperature value can be lowered by approximately 100° C by means of UV irradiation, for example in the case of GaN with a wave length of less than approximately 360nm as illustrated in Fig 1 by the curve b--.

On page 12, at line 3, please amend the paragraph to read as follows:

-- Fig. 2a shows an example of a temperature-time curve of the inventive method. The semiconductor layer or in general the semiconductor layer system is heated from room temperature (point A) as rapidly as possible to the temperature T_1 , at which the sheet resistance of the semiconductor, or the sheet resistance of at least one layer of the semiconductor system, decreases (point B). Now for a first time interval of less than 120 seconds (time difference BE between B and E), the temperature of the layer (or at least one layer of the layer system) is held above the temperature T_1 . Within this first time interval, the layer is heated for the duration of a second time interval (time difference CD between point D and C) to a second temperature T_2 , whereby the second time interval is less than 60 seconds. Within this second time interval, the temperature-time curve can in general have any curve with fluctuations of up to 200° C about the second temperature T_2 . Figure 2a shows for GaN a T_1 of about 550° C and a T_2 greater than the decomposition temperature of about 1050° C, for which reason the second time interval CD is less than 60 seconds, preferably less than 30 seconds. The lower threshold value for the second time interval is in general determined by the (closed loop) control rate with which the increasing portion of the temperature-time curve (ramp up) can be changed into the decreasing portion (ramp down).

For modern RTP systems, this time is approximately 1 second. Under certain conditions, this value can be lowered with future improvements of the RTP systems. The duration of the second time interval CD (in Fig 2a) decidedly determines how much the temperature T_2 can exceed the decomposition temperature T_H without permanently damaging the semiconductor layer or the layer system. The shorter the second time interval CD, the greater can be the temperature T_2 , since then the overall thermal stressing of the layer does not exceed a critical value in the time interval CD. If the thermal stress is below this critical value, then depending upon the type of semiconductor, the vacancies and defects that are formed can to the greatest extent or at least in part be again eliminated by a subsequent annealing (tempering) at a temperature less than T_H . The critical value of the thermal stress of the layer or of the layer system is to be determined experimentally. So that as high a temperature T_2 as possible can be achieved at prescribed critical thermal stress, it is important that the RTP system enable a rapid heating up or cooling off of the semiconductor. Typical maximum heating rates are, depending upon the unit, between 75°C/s and 500°C/s . During the process illustrated in Fig 2a, charge carriers are generated within the layer for the duration of at least one third time interval by electromagnetic radiation, preferably by UV radiation. In this connection, the third time interval can be equal to the first time interval BE, as a result of which the free charge carriers are produced only if the Mg-(or in general foreign atom) hydrogen complexes are already nearly thermally broken up. Due to the (photo) generated free electrons, the diffusion characteristic of the broken-up hydrogen within the semiconductor layer can then be influenced. Alternatively, the third time interval can also embrace the second BE or the overall process AF. In general, the third time interval can be anywhere within the process AF, whereby its duration can be up to the overall process time AF. The lower limit of the third time interval is limited by the technical possibilities of providing an adequate UV light capacity. At the present this limit is approximately 10^{-9}s for pulsed lasers and approximately 10^{-6}s for flash lamps. However, time intervals of 1 to 120 seconds are preferred. It is particularly advantageous to set

the third time interval during the cooling-off phase such that during the cooling of the layer, a repassivation of the foreign atoms (e.g. the Mg in GaN) is extensively prevented. Furthermore, it is also advantageous during the heating-up phase, for example in the range AB or AC, to produce charge carriers by UV irradiation, as a result of which, as illustrated in Fig. 1, the first temperature T_1 , and hence also the thermal stress of the semiconductor, can be reduced. Furthermore, within the process AF, charge carriers can be produced by UV radiation in a plurality of (third) time intervals. For example, the aforementioned advantages can be combined by producing charge carriers between AB or AC in ramp up and in ramp down.--

20. A method of thermally treating at least one layer for actuating foreign atoms passivated in the layer by hydrogen, said method including the steps of:

heating said at least one layer, in a first time interval of less than 120 seconds, above a first temperature at which a specific sheet resistance decreases;

heating said at least one layer, in a second time interval which is within said first time interval and is less than 60 seconds, to above a decomposition temperature of said layer;

producing charge carriers in said at least one layer during at least one third time interval, by electromagnetic radiation.

21. A method according to claim 20, wherein said first temperature is between 300° C and 1200° C.
22. A method according to claim 21, wherein said second temperature is between 600° and 1200° C.
23. A method according to claim 20, wherein thermal method steps carried out beyond said first time interval at temperatures less than said first temperature.
24. A method according to claim 20, wherein said third time interval is partially beyond said first time interval.
25. A method according to claim 20, wherein said time interval is equal to said first time interval.

26. A method according to claim 20, wherein said third time interval is beyond said first time interval.
27. A method according to claim 20, wherein said third time interval encompasses said second time interval.
- 5 28. A method according to claim 20, wherein said charge carriers are produced prior to said second time interval in terms of time.
29. A method according to claim 20, wherein said charge carriers are produced prior to and during said second time interval in terms of time.
- 10 30. A method according claim 20, wherein said charge carriers are produced during and after said second time interval.
31. A method according to claim 20, wherein said charge carriers are produced after said second time interval.
32. A method according to claim 20, wherein said charge carriers are produced only prior to and after said second time interval.
- 15 33. A method according to claim 20, wherein said charge carriers are produced within said second time interval.
34. A method according to claim 20, wherein at least one layer includes compound semiconductors of the group III-V.
- 20 35. A method according to claim 20, wherein at least one layer includes compound semiconductors of the group II-VI.

- 5
- 36. A method according to claim 20, wherein at least one layer includes compound semiconductors of the group III nitrides.
 - 37. A method according to claim 20, wherein the energy of said electromagnetic radiation is greater than an energy gap of at least one layer.
 - 38. A method according to claim 20, wherein thermal treatment of a layer is effected within an RTP system.
 - 39. A method according to claim 20, wherein said second time interval is less than 30 seconds.

UV-Supported Activation of Doping Agent In Compound
Semiconductors Via RTP Systems

5 The present invention relates to a method of thermally treating at least one layer, preferably comprising compound semiconductors, for activating foreign or impurity atoms passivated in the layer by hydrogen, according to which at least one layer is heated for a first time interval of less than 120 seconds to a temperature greater than a first
10 temperature at which the specific layer or sheet resistance decreases.

The described method is used for the electrical activation of the p-doping of II-VI and III-V semiconductors that are produced by CVD (Chemical Vapor Deposition), especially by MOCVD (Metal Organic
15 Chemical Vapor Deposition), processes. Such semiconductors are used, for example, for the manufacture of opto-electronic components (e.g. light emitting components such as blue light diodes or laser diodes). During the CVD processes, in addition to the acceptor hydrogen is also incorporated into the semiconductor layer during the
20 p-doping (e.g. in III-V semiconductors Mg, C, Zn, Be, Cd, Ca, Ba, or in II-VI semiconductors N). Along with the acceptor atoms, the hydrogen form an electrically inactive complex, which leads to a passivation of the acceptor atoms (e.g. Mg) and hence to a high sheet resistance. A number of methods are known for activating the passivated acceptor
25 atoms, according to which the electrically inactive hydrogen-acceptor

complex (or in general hydrogen-foreign atom complex) are broken up and the hydrogen is removed from the p-doped or implanted layer by diffusion.

5 A method according to the initially described type for activating the hydrogen acceptor complexes is described in US 5,786,233, whereby during the method the substrate is irradiated with short wave light, the photon energy of which is greater than the energy gap or band gap at the process temperature. The substrates are preferably heated to
10 temperatures of approximately 650 C to 800 C for a duration of two to thirty minutes. In so doing, the complexes are broken up and the previously passivated acceptors (e.g. GaN:Mg) are activated, as a result of which the sheet resistance is reduced by up to several orders of magnitude, and the hole carrier concentration is correspondingly
15 increased, respectively. As a result of the irradiation with short wavelength light, the activation of the acceptors and hence the hole carrier concentration were significantly increased. Since at still higher temperatures, however, the layer is thermally damaged and the p-diffusivity again decreases as the treatment duration increases,
20 pursuant to US 5,786,233 a longer annealing is preferably used at comparatively low temperatures.

Yoichi Kamiura et al.; Jpn.J.Appl.Phys. 37 (1998) L970-L971 describes the influence of UV irradiation upon the Mg activation of GaN films. In this connection, the GaN films are held in a furnace for about one hour at a temperature greater than an activation temperature of about 550 C. Upon irradiation of the Mg doped GaN layer with UV radiation, the activation temperature can be reduced approximately by 100 C, as a result of which the thermal stress of the substrates is similarly reduced.

In EP 0723303, there is described a light-emitting electronic component that is built up of a hetero structure, and a method for producing the same, according to which at approximately 600 C with the aid of UV laser radiation, an annealing is carried out in order to increase the acceptor activation in the layers and to reduce the sheet resistance, respectively.

In Mamoru Miyachi et al.; Appl.Phys.Lett., Volume 72, No 9, 1101 (1998), there is described the thermal activation of Mg in GaN with the additional generation of charge carriers that are generated by applying a potential to the semiconductor structure. In addition, reference is made to the fact that a p-type or conducting characteristic of GaN that contains Mg is also achieved and can be influenced by an irradiation with low energy electrons.

5 All of the previously described methods serve for the activation of the hydrogen-passivated acceptors. The drawback of the above described methods is that relatively long process times are necessary for the activation, whereby in general the substrates (e.g. Sapphire, SiC, Si, AlN, ZnO or Al₂O₃), with the semiconductor films disposed thereon, are subjected to a high thermal stress, and in addition the throughput is very low.

10 It is an object of the present invention to eliminate these drawbacks.

15 Pursuant to the present invention, this object is realized in that with the initially described method, within the first time interval at least one layer is heated for a second time interval of up to 60 seconds to a second temperature that is greater than the first temperature, whereby during the method in at least one third time interval charge carriers are produced within the layer by electromagnetic radiation.

20 By means of the inventive method, the process duration is advantageously considerably shortened for the activation of the hydrogen-passivated foreign atoms (e.g. Mg) in one or more layers (e.g. GaN) that are comprised of compound semiconductors, whereby sheet resistance and hole carrier concentration are comparable with the aforementioned known methods.

The inventive method is preferably carried out in rapid heating or RTP (Rapid Thermal Processing) systems, since in RTP systems the semiconductor can be processed with very precise temperature-time processes and very high uniformity.

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The first temperature of the inventive method is selected between 350 C and 900 C, whereby for example with Mg-containing GaN (or in general with group III nitrides) a temperature between 350 C and 600 C is preferred. Depending upon the type of semiconductor, the first temperature can also be a function of the selection of the third time interval and of the intensity of the electromagnetic radiation and of the generation of minority charge carriers connected therewith. As the length of the third time interval increases, and with increasing intensity of the electromagnetic radiation, then depending upon the type of semiconductor the first temperature can be decreased, which advantageously leads to a reduction of the thermal stress of the layer.

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The second temperature during the second time interval is preferably selected between 700 C and 1400 C. The selection of this temperature depends to a considerable extent upon the material of the compound semiconductor, whereby for example with Mg-containing GaN a second temperature between 850 C and 1200 C is preferably selected. By selecting a higher second temperature, the second time interval can

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preferably be considerably shortened, which similarly again leads to a reduction of the thermal stressing of the semiconductor layer or of the semiconductor layer system.

5 Heating the semiconductor to a second temperature that is greater than the decomposition temperature can be effected for a short period of time. If the surface of the semiconductor layer is provided with a coating (e.g. SiO_2), or if the semiconductor layer is heated at an overpressure, for example in a hydrogen-free N_2 atmosphere, a
10 decomposition of the compound semiconductor takes place only at higher temperatures, as a result of which the second temperature can be further increased. In this connection, time and temperature are selected in such a way that, for example in the case of Mg-doped GaN, the donor centers that result from nitrogen vacancies do not exceed the
15 number of active Mg centers (in general active activator centers), so that in particular a p-type or conducting characteristic of the layer results. As a consequence, there is provided the possibility of setting or establishing the concentration and activation of donor and acceptor centers in wide ranges.

20 The duration of the third time interval, during which charge carriers are produced within the semiconductor layers by electromagnetic radiation, can be the same as the duration of the first time interval. In this

connection, during the overall process portion of the method during which the semiconductor layer has a temperature greater than the first temperature, minority charge carriers are generated.

5 The third time interval can, however, also be disposed entirely or partially outside of the first time interval. The layer is then irradiated also or only during the heating and/or cooling phase, or another desired temperature-time process step, which is disposed prior to or after the first time interval, within which the layer temperature is still
10 below the first temperature. This is in particular advantageous if a repassivation of activated foreign atom (e.g. Mg) by hydrogen is to be avoided during the cooling, or if as described above the first temperature is already to be achieved earlier, i.e. at a lower temperature.

15 On the whole, as a result of the position and the length of the second and third time intervals, as well as a result of the first and second temperature, also the spatial concentration profile of the activated and passivated foreign atoms can be set or established. If the second
20 temperature is above the decomposition temperature, the defect concentration and the spatial distribution thereof can also be set or established. Thus, for example, the third time interval can encompass the second, can be the same or within the second time interval, can be

disposed prior to the second time interval, can encompass time ranges prior to and beyond the second time interval or time ranges from the second and after the second time interval, or can also encompass time intervals after the second time interval.

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The layers can include group III-V and/or group II-VI compound semiconductors, especially group III nitrides such as, for example, GaN.

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The energy of the electromagnetic radiation of the inventive method is advantageously greater than the energy gap of at least one layer. As a result, minority carriers are produced within the layer by the electromagnetic radiation, thereby facilitating the activation of the hydrogen-passivated foreign atoms, avoiding a repassivation of the foreign atoms, and supporting the diffusion of the hydrogen.

15

The inventive method is advantageously carried out, as mentioned above, by means of a rapid heating system (RTP System), since by means of the RTP system defined very short heating processes can be carried out in the range of one second up to 30 minutes. The temperature-time curves of the substrates can be established precisely to the second in a temperature range of room temperature to about 1400 C, whereby the substrate can be heated extremely uniformly not

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only at low temperatures but also at high temperatures. Similarly, in RTP systems various process gases, which surround the substrate, can also be used, whereby the process gas pressure can be adjusted from vacuum conditions to overpressure conditions.

The present invention will be described in detail in the following with reference to the following figures. The drawings show:

Fig. 1 schematically the sheet resistance as a function of the temperature and the influence of electromagnetic radiation thereupon,

Fig. 2a – 2f various schematically illustrated temperature-time curves for a semiconductor layer treated in an RTP system.

In Fig. 1, curve a schematically shows a typical plot of a sheet resistance of a compound semiconductor, e.g. a GaN film that is treated or doped with Mg, as a function of the temperature. At low temperatures (e.g. GaN at less than approximately 550 C), the semiconductor film has a high electrical resistance R due to the passivation of the foreign atoms (e.g. Mg) by hydrogen. As the temperature increases, the resistance is reduced since more and more hydrogen-acceptor (foreign atom) complexes break up and the acceptors are thus electrically activated. If all of the complexes are broken up, and all acceptors activated, as the temperature increases from a lower temperature value T_L to an upper temperature value T_H , the sheet resistance remains nearly constant. Above the upper temperature value T_H , the sheet resistance R again significantly increases as the temperature increases, since above this temperature T_H a decomposition of the semiconductor layer occurs. For Mg doped GaN, at atmospheric pressure, T_L is approximately 550 C and T_H is approximately 1050 C.

By generating minority charge carriers, the temperature T_L can be reduced. In this connection, the charge carriers can be generated, for example, by irradiation with UV light, whereby the irradiated photon

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energy is greater than the energy gap of the semiconductor layer at the appropriate layer temperature. However, the charge carriers can also be generated, for example, by applying a potential or voltage to a semiconductor layer or to a semiconductor layer system, or by a combination of UV irradiation and applying a potential or a voltage to the layer or the layer system. The reduction of the lower temperature value T_L depends upon the number of charge carriers generated. The lower temperature value can be lowered by approximately 100 C by means of UV irradiation, for example in the case of GaN with a wave length of less than approximately 360nm as illustrated in Fig 1 by the curve b.

Fig. 2a shows an example of a temperature-time curve of the inventive method. The semiconductor layer or in general the semiconductor layer system is heated from room temperature (point A) as rapidly as possible to the temperature T_1 , at which the sheet resistance of the semiconductor, or the sheet resistance of at least one layer of the semiconductor system, decreases (point B). Now for a first time interval of less than 120 seconds (time difference BE between B and E), the temperature of the layer (or at least one layer of the layer system) is held above the temperature T_1 . Within this first time interval, the layer is heated for the duration of a second time interval (time difference CD between point D and C) to a second temperature T_2 ,

whereby the second time interval is less than 60 seconds. Within this second time interval, the temperature-time curve can in general have any curve with fluctuations of up to 200 C about the second temperature T_2 . Figure 2a shows for GaN a T_1 of about 550 C and a
5 T_2 greater than the decomposition temperature of about 1050 C, for which reason the second time interval CD is less than 60 seconds, preferably less than 30 seconds. The lower threshold value for the second time interval is in general determined by the (closed loop) control rate with which the increasing portion of the temperature-time
10 curve (ramp up) can be changed into the decreasing portion (ramp down). For modern RTP systems, this time is approximately 1 second. Under certain conditions, this value can be lowered with future improvements of the RTP systems. The duration of the second time interval CD (in Fig 2a) decidedly determines how much the
15 temperature T_2 can exceed the decomposition temperature T_H without permanently damaging the semiconductor layer or the layer system. The shorter the second time interval CD, the greater can be the temperature T_2 , since then the overall thermal stressing of the layer does not exceed a critical value in the time interval CD. If the thermal
20 stress is below this critical value, then depending upon the type of semiconductor, the vacancies and defects that are formed can to the greatest extent or at least in part be again eliminated by a subsequent annealing (tempering) at a temperature less than T_H . The critical value

of the thermal stress of the layer or of the layer system is to be determined experimentally. So that as high a temperature T_2 as possible can be achieved at prescribed critical thermal stress, it is important that the RTP system enable a rapid heating up or cooling off of the semiconductor. Typical maximum heating rates are, depending upon the unit, between 75 C/s and 500 C/s. During the process illustrated in Fig 2a, charge carriers are generated within the layer for the duration of at least one third time interval by electromagnetic radiation, preferably by UV radiation. In this connection, the third time interval can be equal to the first time interval BE, as a result of which the free charge carriers are produced only if the Mg-(or in general foreign atom) hydrogen complexes are already nearly thermally broken up. Due to the (photo) generated free electrons, the diffusion characteristic of the broken-up hydrogen within the semiconductor layer can then be influenced. Alternatively, the third time interval can also embrace the second BE or the overall process AF. In general, the third time interval can be anywhere within the process AF, whereby its duration can be up to the overall process time AF. The lower limit of the third time interval is limited by the technical possibilities of providing an adequate UV light capacity. At the present this limit is approximately 10^{-9} s for pulsed lasers and approximately 10^{-6} s for flash lamps. However, time intervals of 1 to 120 seconds are preferred. It is particularly advantageous to set the third time interval during the

cooling-off phase such that during the cooling of the layer, a
repassivation of the foreign atoms (e.g. the Mg in GaN) is extensively
prevented. Furthermore, it is also advantageous during the heating-up
phase, for example in the range AB or AC, to produce charge carriers
by UV irradiation, as a result of which, as illustrated in Fig. 1, the first
temperature T_1 , and hence also the thermal stress of the
semiconductor, can be reduced. Furthermore, within the process AF,
charge carriers can be produced by UV radiation in a plurality of (third)
time intervals. For example, the aforementioned advantages can be
combined by producing charge carriers between AB or AC in ramp up
and in ramp down.

Fig. 2b shows a further example of the inventive method, according to
which the temperature T_2 is below the decomposition temperature T_H .
Here also at least one semiconductor layer is heated for at least one
time interval BE of less than 120s to a temperature greater than the
first temperature T_1 and for a second time interval CD of less than 60s
is held at a temperature T_2 . The length of the second time interval CD
is here less critical since the second temperature T_2 is below the
composition temperature T_H . The position and the duration of the third
time interval can be selected in conformity with the foregoing
discussion in conjunction with Fig. 2a. The position and duration of the
third time interval is optimized as a function of the influence of the

diffusion of the hydrogen, which results during the consumption of the Mg (foreign atom) hydrogen complex, by the free charge carriers generated by the UV radiation. This is to be determined experimentally and depends upon the semiconductor layer. Thus, for example, it can be advantageous, during the process range AB (in Fig 2b), to irradiate the layer with UV light. As a result, the Mg-H complex is already broken up at low temperatures. During the process range BD, the hydrogen diffuses at very high diffusion constants into the surface regions of the layer. During the cooling-off phase DF, a renewed UV irradiation is effected in order to prevent a repassivation of the Mg (the foreign atoms) in GaN (in II-VI or III-V semiconductors). On the whole, with this method one obtains a high degree of activation.

A further embodiment is illustrated in Fig 2c. In contrast to the previous examples, the semiconductor layer is tempered in the temperature range between T_1 and T_H for a time interval CD of less than 120s. During this time, the activation of the foreign atoms is effected by breaking up the hydrogen-foreign atom complex and the hydrogen or the hydrogen ions diffused in the direction of the semiconductor surface, respectively. As a result of the further temperature increase DEF the hydrogen diffusion is increased. In this connection, the temperature can exceed the decomposition temperature T_H , as illustrated in Fig 2c, or can be below this temperature as shown in Fig

2d. Here also, the rate of activation or repassivation (or the spatial distributions thereof) can be influenced by the position and duration of the third time interval and by irradiation with UV light. In the examples of Figs 2c and Fig 2d, not only the time interval CD but also the time interval EF with the respectively pertaining (average) temperature can be seen as the second time interval and the second temperature T_2 , respectively. Due to the diffusion and activation mechanism, the time interval EF of the higher temperature is preferably considerably shorter than the time interval CD. The intervals are together shorter than 120s, which is the maximum permissible time for the first time interval BG.

A further specific embodiment of the present invention is illustrated in Fig 2e. In contrast to Fig 2c and Fig 2d, here a further temperature step in the interval GH follows a high temperature EF, which is preceded by a lower temperature step CD (approximately as in Fig. 2c). This step GH is particularly advantageous if appreciable defect formation (e.g. N-vacancies in GaN) occurs due to a high temperature step EF (which can also be disposed above the decomposition temperature). Depending upon the semiconductor material, these defects can extensively or at least partially be cured in the step GH. Also in this example, not only the time interval CD but also the time intervals EF and/or GH can be interpreted as the second time interval or as second time intervals, whereby the interval length is then less

than 60s. The sum of the intervals CD, EF and GH are similarly less than 120s, which is the maximum permissible time for the first time interval BI. With regard to the position and duration of the third time interval, within which charge carriers are produced in the semiconductor layer by electromagnetic radiation (e.g. UV light), in a manner analogous to the preceding specific examples, the rate of activation or repassivation (and the distributions thereof, respectively) can be influenced by irradiation with UV light. For this purpose, the position and the duration of the third time interval is suitably selected within the process AK, whereby as described above, the semiconductor layer is preferably irradiated with UV light in the range of the heating up, in other words for example in the range AB or AC, and in the ranges of the cooling off, in other words for example in the ranges FG and/or HI.

By applying an electrical field to the semiconductor layer, the diffusion process of the hydrogen resulting during the activation can also be influenced, i.e. the diffusion through the semiconductor layer can be accelerated. This can be effected, for example, in that the substrate with the semiconductor layer and the semiconductor layer system, respectively, is provided with a positive potential with respect to the process gas, or with respect to an electrode that is spaced over the layer. Such an arrangement, or such a method, can be used by itself

or in combination with the previously illustrated method. In general, in the third time interval, in addition to the electromagnetic radiation, a potential can be used in the described type and manner, or during the third time interval only a potential can be utilized for all of the
5 aforementioned cases, i.e., a UV irradiation is dispensed with. If during the third time interval only a potential or a field is applied to the layer, this does not necessarily produce charge carriers in the layer. Already the change of the electrochemical potential alters the diffusion of the hydrogen in the layer.

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PATENT CLAIMS:

1. Method for the thermal treatment of at least one layer, preferably comprising compound semiconductors, for the activation of foreign atoms passivated in the layer by hydrogen, having the method steps:

heating at least one layer, for a first time interval of less than 120 sec, to a temperature greater than a first temperature at which the specific sheet resistance decreases,

within the first time interval, heating at least one layer, for a second time interval of up to 60 sec, to a second temperature greater than the first temperature,

whereby during the method, in at least one third time interval, producing charge carriers are by electromagnetic radiation within said layer.

2. Method according to claim 1, characterized in that the first temperature is between 300 C and 1200 C.

3. Method according to claim 1 or 2, characterized in that the second temperature is between 600 C and 1200 C.
4. Method according to claim 1 to 3, characterized in that thermal method steps that are beyond the first time interval are provided at temperatures T less than T_1 .
5. Method according to one of the claims 1 to 4, characterized in that the third time interval is partially beyond the first time interval.
6. Method according to one of the previous claims, characterized in that the third time interval is equal to the first.
7. Method according to one of the claims 1 to 5, characterized in that the third time interval is beyond the first time interval.
8. Method according to one of the claims 1 to 5, characterized in that the third time interval encompasses the second time interval.

9. Method according to one of the claims 1 to 5, characterized in that the charge carriers are formed by electromagnetic radiation prior to the second time interval in terms of time.

5 10. Method according to one of the claims 1 to 5, characterized in that the charge carriers are formed by electromagnetic radiation prior to and during the second time interval in terms of time.

10 11. Method according to one of the claims 1 to 5, characterized in that the charge carriers are formed by electromagnetic radiation during and after the second time interval.

15 12. Method according to one of the claims 1 to 5, characterized in that the charge carriers are formed by electromagnetic radiation after the second time interval.

20 13. Method according to one of the claims 1 to 5, characterized in that the charge carriers are formed by electromagnetic radiation only prior to and after the second time interval.

14. Method according to one of the claims 1 to 5, characterized in that the charge carriers are formed by electromagnetic radiation within the second time interval.

15. Method according to one of the claims 1 to 14, characterized in that at least one layer includes compound semiconductors of the group III-V.

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16. Method according to one of the claims 1 to 15, characterized in that at least one layer includes compound semiconductors of the group II-VI.

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17. Method according to claim 16, characterized in that at least one layer includes compound semiconductors of the group III nitrides.

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18. Method according to at least one of the preceding claims, characterized in that the energy of the electromagnetic radiation is greater than the energy gap of at least one layer.

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19. Method according to at least one of the preceding claims, characterized in that the thermal treatment of the layer is effected within an RTP system.

PCT

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 INTERNATIONALE ANMELDUNG VERÖFFENTLICHT NACH DEM VERTRAG ÜBER DIE
 INTERNATIONALE ZUSAMMENARBEIT AUF DEM GEBIET DES PATENTWESENS (PCT)

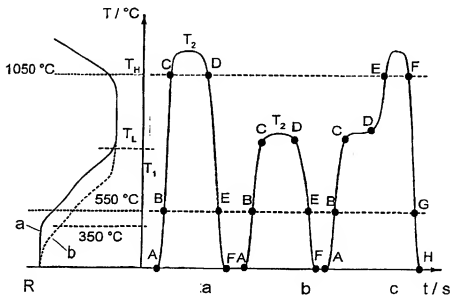
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(54) Title: UV-SUPPORTED ACTIVATION OF A DOPING AGENT IN COMPOUND SEMICONDUCTORS BY MEANS OF RTP SYSTEMS

(54) Bezeichnung: UV-UNTERSTÜTZTE DOTIERSTOFFAKTIVIERUNG IN VERBINDUNGSHALBLEITERN MITTELS RTP-SYSTEMEN

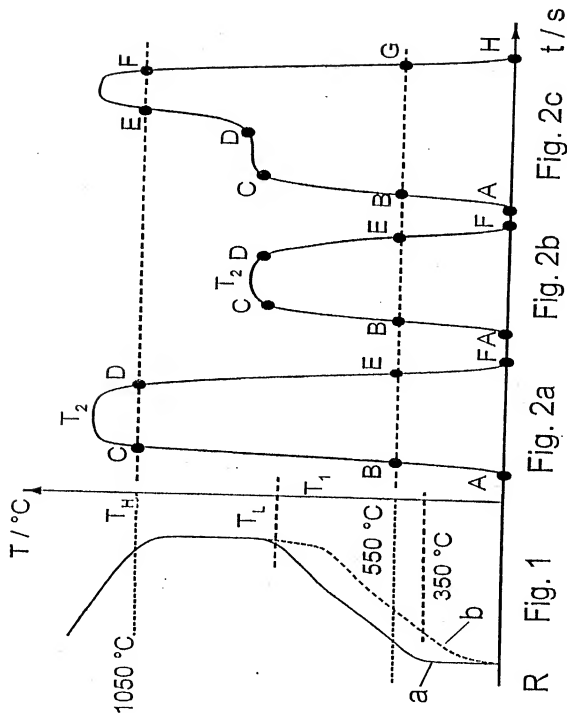
(57) Abstract

A method for thermal treatment of at least one layer, preferably consisting of compound semiconductors, whereby hydrogen-passivated impurity atoms in said layer are activated. According to the inventive method, a layer is heated during a time period of less than 120 seconds to a temperature which is higher than the first temperature (T₁) at which specific layer resistance decreases, whereby at least one layer is heated during the first time period to a second temperature (T₂) which is higher than the first temperature for up to 60 seconds. Charge carriers are produced inside said layer during the course of the inventive method by means of electromagnetic radiation during at least one third time period. The method is particularly suitable of use in the activation of Mg acceptors in GaN.



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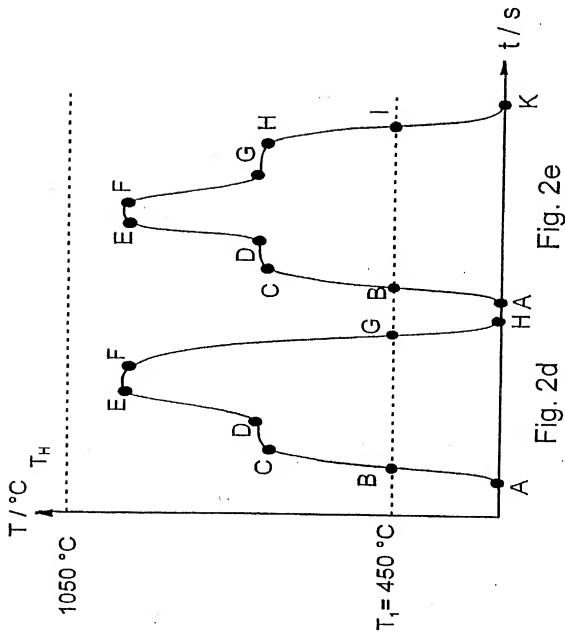
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R Fig. 1 Fig. 2a Fig. 2b Fig. 2c

Martin Drechsler, et al

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COMBINED DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As below named inventors, we hereby declare that:

Our residences, post office addresses and citizenships are as stated below next to our names; we believe are the original, first and joint inventors of the subject matter which is claimed and for which a patent is sought of the invention entitled:

UV SUPPORTED THERMAL TREATMENT OF COMPOUND SEMICONDUCTORS
IN RTP SYSTEMS
the specification of which

is attached hereto;

XX was filed on 22 April 2000 as International Application Ser. No. PCT/EP00/03666 and is amended herewith. as Serial No. 10/069,826

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose all information known by me to be material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Applications:

Priority
Claimed:

199 20 871.9	Germany	06 May 1999	X
(Number)	(Country)	(Day/Month/Year Filed)	Yes No

I hereby claim the benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below:

(Application Number) (Filing Date)

I hereby appoint attorney Robert W. Becker, Reg. No. 26,255, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith. Address all telephone calls to (505) 286-3511. Address all correspondence to ROBERT W. BECKER & ASSOCIATES, 11896 N. Highway 14, Suite B, Tijeras, New Mexico 87059.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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